《Cryptographic Hardwa》

图书基本信息

书名：《Cryptographic Hardware and Embedded Systems - CHES 2005加密硬件与嵌入系统 - CHES 2005 / 会议录》

13位ISBN编号：9783540284741

10位ISBN编号：3540284745

出版时间：2005-9

出版社：北京燕山出版社

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页数：458

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内容概要

This book constitutes the refereed proceedings of the 7th International Workshop on Cryptographic Hardware and Embedded Systems, CHES 2005, held in Edinburgh, UK in August/September 2005. The 32 revised full papers presented were carefully reviewed and selected from 108 submissions. The papers are organized in topical sections on side channels, arithmetic for cryptanalysis, low resources, special purpose hardware, hardware attacks and countermeasures, arithmetic for cryptography, trusted computing, and efficient hardware.

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书籍目录

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