

《Advances in Computer》

图书基本信息

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内容概要

This book constitutes the refereed proceedings of the 10th Asia-Pacific Computer Systems Architecture Conference, ACSAC 2005, held in Singapore in October 2005. The 65 revised full papers presented were carefully reviewed and selected from 173 submissions. The papers are organized in topical sections on energy efficient and power aware techniques, methodologies and architectures for application-specific systems, processor architectures and microarchitectures, high-reliability and fault-tolerant architectures, compiler and OS for emerging architectures, data value predictions, reconfigurable computing systems and polymorphic architectures, interconnect networks and network interfaces, parallel architectures and computation models, hardware-software partitioning, verification, and testing of complex architectures, architectures for secured computing, simulation and performance evaluation, architectures for emerging technologies and applications, and memory systems hierarchy and management.

书籍目录

Keynote Address I Processor Architecture for Trustworthy Computers Session 1A: Energy Efficient and Power Aware Techniques Efficient Voltage Scheduling and Energy-Aware Co-synthesis for Real-Time Embedded Systems Energy-Effective Instruction Fetch Unit for Wide Issue Processors Rule-Based Power-Balanced VLIW Instruction Scheduling with Uncertainty An Innovative Instruction Cache for Embedded Processors Dynamic Voltage Scaling for Power Aware Fast Fourier Transform (FFT) Processor David Fitrio, Jugdutt (Jack) Singh, Aleksandar (Alex) Stojcevski Session 1B" Methodologies and Architectures for Application-Specific Systems Design of an Efficient Multiplier-Less Architecture for Multi-dimensional Convolution A Pipelined Hardware Architecture for Motion Estimation of H.264/AVC Embedded Intelligent Imaging On-Board Small Satellites Architectural Enhancements for Color Image and Video Processing on Embedded Systems A Portable Doppler Device Based on a DSP with High- Performance Spectral Estimation and Output Session 2A: Processor Architectures and Microarchitectures A Power-Efficient Processor Core for Reactive Embedded Applications A Stream Architecture Supporting Multiple Stream Execution Models The Challenges of Massive On-Chip Concurrency FMRPU: Design of Fine-Grain Multi-context Reconfigurable Processing Unit Session 2B: High-Reliability and Fault-Tolerant Architectures Modularized Redundant Parallel Virtual File System Resource-Driven Optimizations for Transient-Fault Detecting SuperScalar Microarchitectures A Fault-Tolerant Routing Strategy for Fibonacci-Class Cubes Embedding of Cycles in the Faulty Hypercube Session 3A: Compiler and OS for Emerging Architectures.....Keynote Address IIAuthor Index

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